infineon

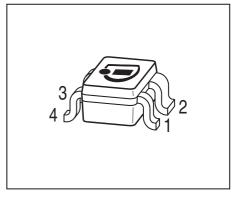
Low Noise SiGe:C Bipolar RF Transistor

- Highly linear low noise RF transistor
- Provides outstanding performance for a wide range of wireless applications
- Based on Infineon's reliable high volume Silicon Germanium technology
- Ideal for CDMA and WLAN applications
- Collector design provides high linearity of 14.5 dBm OP1dB for low voltage application
- Maximum stable gain
 - G_{ms} = 21.5 dB at 1.8 GHz
 - G_{ma} = 11 dB at 6 GHz
- Outstanding noise figure NF_{min} = 0.7 dB at 1.8 GHz Outstanding noise figure NF_{min} = 1.3 dB at 6 GHz
- Accurate SPICE GP model enables effective design in process
- Pb-free (RoHS compliant) and halogen-free package with visible leads
- Qualification report according to AEC-Q101 available



ESD (Electrostatic discharge) sensitive device, observe handling precaution!

Туре	Marking	Pin Configuration			Package			
BFP620	R2s	1=B	2=E	3=C	4=E	-	-	SOT343



BFP620



Maximum R	atings at T _A	= 25 °C I	unless otherwise	e specified
	alingsatia	-200,0		specifica

Parameter	Symbol	Value	Unit
Collector-emitter voltage	V _{CEO}		V
<i>T</i> _A = 25 °C		2.3	
<i>T</i> _A = -55 °C		2.1	
Collector-emitter voltage	V _{CES}	7.5	
Collector-base voltage	V _{CBO}	7.5	
Emitter-base voltage	V _{EBO}	1.2	
Collector current	I _C	80	mA
Base current	I _B	3	
Total power dissipation ¹⁾	P _{tot}	185	mW
<i>T</i> _S ≤ 95°C			
Junction temperature	TJ	150	°C
Ambient temperature	T _A	-65 150	
Storage temperature	T _{Stg}	-65 150	

Thermal Resistance

Parameter	Symbol	Value	Unit
Junction - soldering point ²⁾	R _{thJS}	300	K/W

Electrical Characteristics at T_A = 25 °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics	·				
Collector-emitter breakdown voltage	V _{(BR)CEO}	2.3	2.8	-	V
<i>I</i> _C = 1 mA, <i>I</i> _B = 0					
Collector-emitter cutoff current	I _{CES}				μA
$V_{\rm CE}$ = 7.5 V, $V_{\rm BE}$ = 0		-	-	10	
$V_{\rm CE}$ = 5 V, $V_{\rm BE}$ = 0		-	0.001	0.04	
Collector-base cutoff current	I _{CBO}	-	1	40	nA
$V_{\rm CB}$ = 5 V, $I_{\rm E}$ = 0					
Emitter-base cutoff current	I _{EBO}	-	10	900	
V _{EB} = 0.5 V, <i>I</i> _C = 0					
DC current gain	h _{FE}	110	180	270	-
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 1.5 V, pulse measured					

 $^{1}\mathcal{T}_{S}$ is measured on the emitter lead at the soldering point to the pcb

²For the definition of R_{thJS} please refer to Application Note AN077 (Thermal Resistance Calculation)



Parameter	Symbol	Values			Unit
		min.	typ.	max.	
AC Characteristics (verified by random sampling	g)	1			1
Transition frequency	f _T	-	65	-	GHz
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 1.5 V, f = 1 GHz					
Collector-base capacitance	C _{cb}	-	0.12	0.2	pF
$V_{\rm CB} = 2 \text{ V}, f = 1 \text{ MHz}, V_{\rm BE} = 0$,					
emitter grounded					
Collector emitter capacitance	C _{ce}	-	0.22	-	
$V_{CE} = 2 V, f = 1 MHz, V_{BE} = 0$,					
base grounded					
Emitter-base capacitance	C _{eb}	-	0.46	-	
$V_{\rm EB}$ = 0.5 V, f = 1 MHz, $V_{\rm CB}$ = 0 ,					
collector grounded					
Minimum noise figure	NF _{min}				dB
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 1.5 V, f = 1.8 GHz, $Z_{\rm S}$ = $Z_{\rm Sopt}$		-	0.7	-	
$I_{\rm C}$ = 5 mA, $V_{\rm CE}$ = 1.5 V, f = 6 GHz, $Z_{\rm S}$ = $Z_{\rm Sopt}$		-	1.3	-	
Power gain, maximum stable ¹⁾	G _{ms}	-	21.5	-	dB
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 1.5 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
$Z_{\rm L} = Z_{\rm Lopt}$, $f = 1.8 \rm GHz$					
Power gain, maximum available ¹⁾	G _{ma}	-	11	-	dB
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 1.5 V, $Z_{\rm S}$ = $Z_{\rm Sopt}$,					
$Z_{\rm L} = Z_{\rm Lopt}, f = 6 {\rm GHz}$					
Transducer gain	S _{21e} ²				dB
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 1.5 V, $Z_{\rm S}$ = $Z_{\rm L}$ = 50 Ω,					
<i>f</i> = 1.8 GHz		-	20	-	
<i>f</i> = 6 GHz		-	9.5	-	
Third order intercept point at output ²⁾	IP3	-	25.5	-	dBm
V_{CE} = 2 V, I_C = 50 mA, Z_S = Z_L =50 Ω, f = 1.8 GHz					
1dB compression point at output	P _{-1dB}	-	14.5	-	
$I_{\rm C}$ = 50 mA, $V_{\rm CE}$ = 2 V, $Z_{\rm S}$ = $Z_{\rm L}$ =50 Ω , f = 1.8 GHz					

Electrical Characteristics at T_A = 25 °C, unless otherwise specified

 ${}^{1}G_{\mathsf{ma}} = |S_{21\mathrm{e}} / S_{12\mathrm{e}}| \; (\mathrm{k} \cdot (\mathrm{k}^{2} \cdot 1)^{1/2}), \; G_{\mathsf{ms}} = |S_{21\mathrm{e}} / S_{12\mathrm{e}}|$

²IP3 value depends on termination of all intermodulation frequency components.

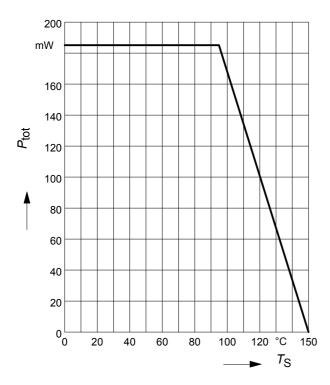
Termination used for this measurement is 50 Ω from 0.1 MHz to 6 GHz



BFP620

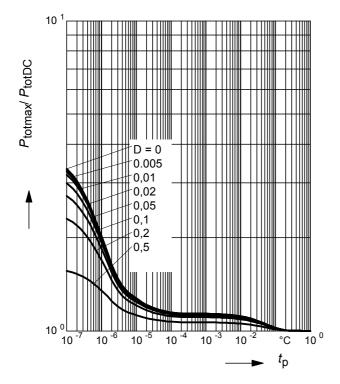
Total power dissipation $P_{tot} = f(T_S)$

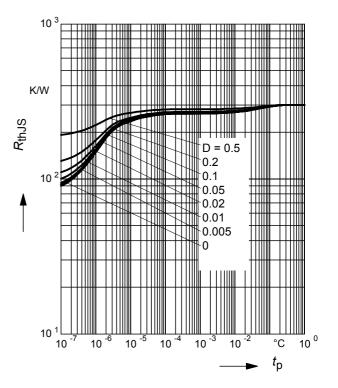
Permissible Pulse Load $R_{\text{thJS}} = f(t_p)$



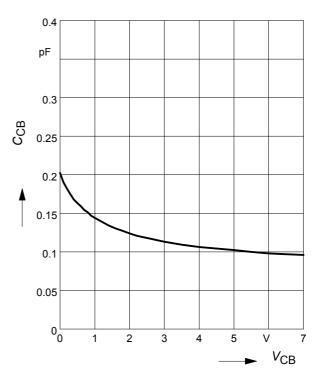
Permissible Pulse Load

 $P_{\text{totmax}}/P_{\text{totDC}} = f(t_{\text{p}})$





Collector-base capacitance C_{CD} = $f(V_{CB})$ f = 1MHz

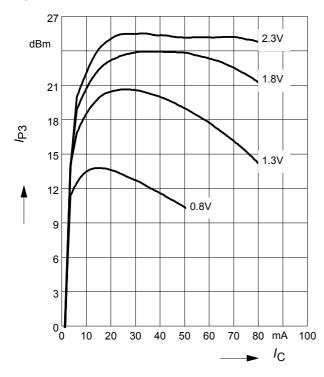




Third order Intercept Point $IP3 = f(I_C)$

(Output, $Z_S = Z_L = 50 \Omega$)

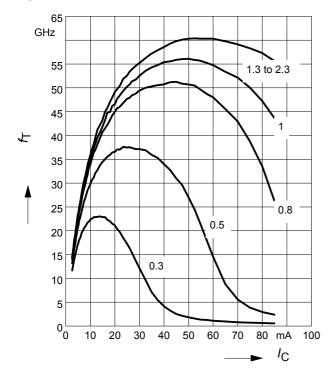
 V_{CE} = parameter, f = 900 MHz



Transition frequency $f_{\rm T}$ = $f(I_{\rm C})$

f = 1 GHz

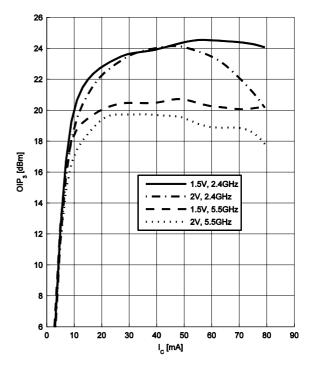




Third order Intercept Point $IP3 = f(I_C)$

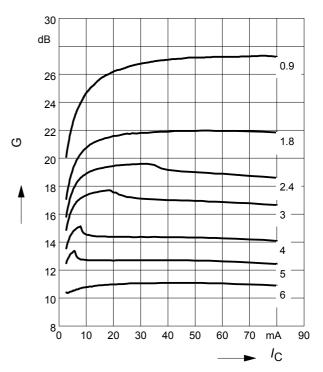
(Output, $Z_{\rm S} = Z_{\rm L} = 50 \ \Omega$)

 V_{CE} = parameter, f = parameter

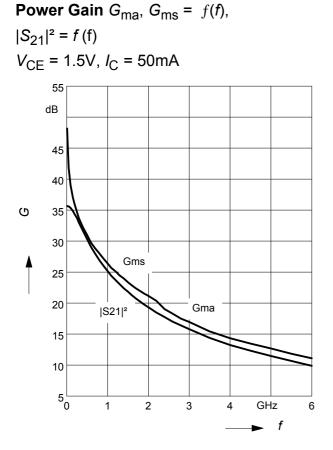


Power gain G_{ma} , $G_{ms} = f(I_C)$ $V_{CE} = 1.5V$

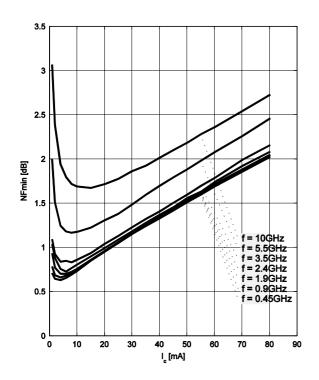








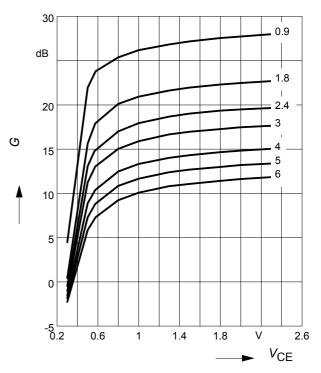
Minimum noise figure $NF_{min} = f(I_C)$ $V_{CE} = 2 V, Z_S = Z_{Sopt}$



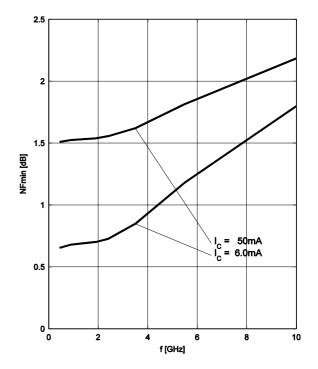
Power gain G_{ma} , $G_{ms} = f(V_{CE})$

 $I_{\rm C}$ = 50mA

f = Parameter in GHz



Minimum noise figure $NF_{min} = f(f)$ $V_{CE} = 2V, Z_S = Z_{Sopt}$

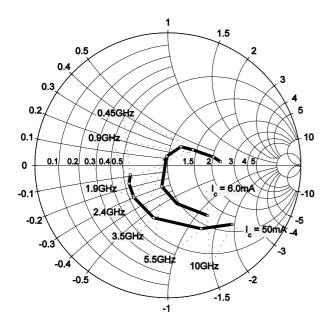




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Source impedance for min. noise figure vs. frequency

 $V_{\rm CE}$ = 2 V, $I_{\rm C}$ = 6 mA / 50 mA



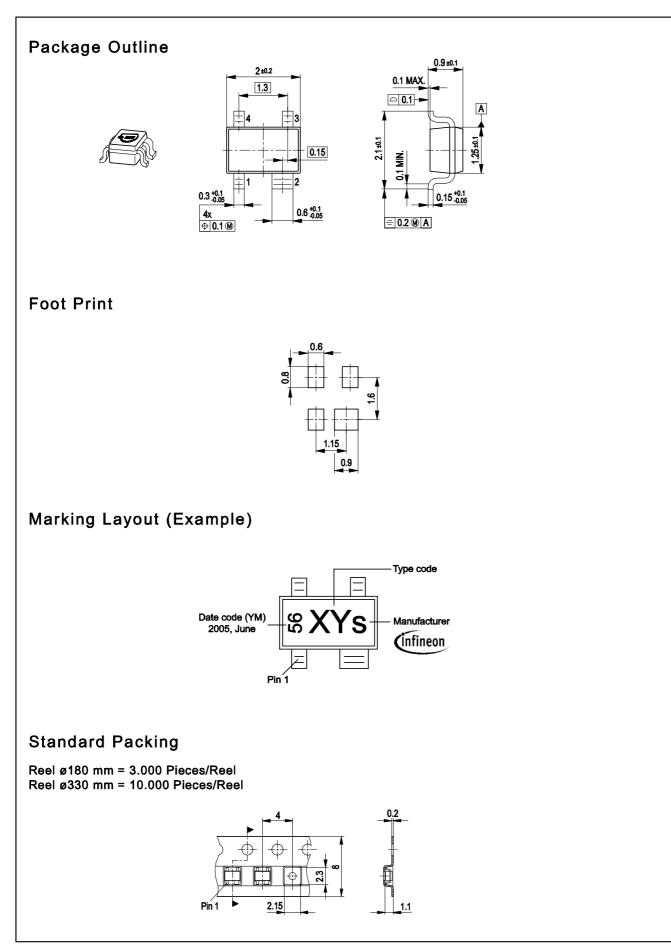


SPICE GP Model

For the SPICE Gummel Poon (GP) model as well as for the S-parameters (including noise parameters) please refer to our internet website www.infineon.com/rf.models.

Please consult our website and download the latest versions before actually starting your design. You find the BFP620 SPICE GP model in the internet in MWO- and ADS-format, which you can import into these circuit simulation tools very quickly and conveniently. The model already contains the package parasitics and is ready to use for DC and high frequency simulations. The terminals of the model circuit correspond to the pin configuration of the device. The model parameters have been extracted and verified up to 15 GHz using typical devices. The BFP620 SPICE GP model reflects the typical DC- and RF-performance within the limitations which are given by the SPICE GP model itself. Besides the DC characteristics all S-parameters in magnitude and phase, as well as noise figure (including optimum source impedance, equivalent noise resistance and flicker noise) and intermodulation have been extracted.









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